

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently Amended) A voltage conversion circuit comprising:

a pulse generator for generating a pulse signal having a fixed pulse width at variable pulse periods,

wherein the pulse signal from the pulse generator is fed into a voltage conversion section ~~wherein~~ whereby an output voltage from the voltage conversion ~~circuit~~ section is determined according to a ratio of the pulse width to the pulse period of the pulse signal generated by the pulse generator.

2. (Currently Amended) The voltage conversion circuit as claimed in claim 1,

wherein the variation of the pulse period of the pulse signal is controlled by a delay circuit, a minimum delay produced from the delay circuit minimizes the pulse period ~~reduced~~ by restricting a range within which the output voltage from the voltage conversion ~~circuit~~ section is variable.

3. (Currently Amended) The voltage conversion circuit as claimed in claim 2,

wherein an upper limit of the range within which the output voltage from the voltage conversion ~~circuit~~ section is variable is equal to or lower than half a voltage amplitude of the pulse signal.

4. (Currently Amended) The voltage conversion circuit as claimed in claim 2,

wherein the range within which the output voltage from the voltage conversion ~~circuit~~ section is variable is within $\pm 20\%$ of ~~an optimum~~ a medium operating voltage of the output voltage.

5. (Original) The voltage conversion circuit as claimed in claim 1,

wherein the output voltage is selected from among discrete values within the range within which the output voltage is variable.

6. (Original) The voltage conversion circuit as claimed in claim 1,

wherein the pulse generator varies the pulse period of the pulse signal by giving a predetermined delay to a reference pulse signal generated within the pulse generator, the reference pulse signal having a fixed pulse width.

7. (currently amended) A semiconductor integrated circuit device comprising:

a voltage conversion circuit as claimed in claim 1, the voltage conversion circuit comprising:

a pulse generator for generating a pulse signal having a fixed pulse width at variable pulse periods,

wherein the pulse signal from the pulse generator is fed into a voltage conversion section whereby an output voltage

from the voltage conversion section is determined according to
a ratio of the pulse width to the pulse period of the pulse
signal generated by the pulse generator.